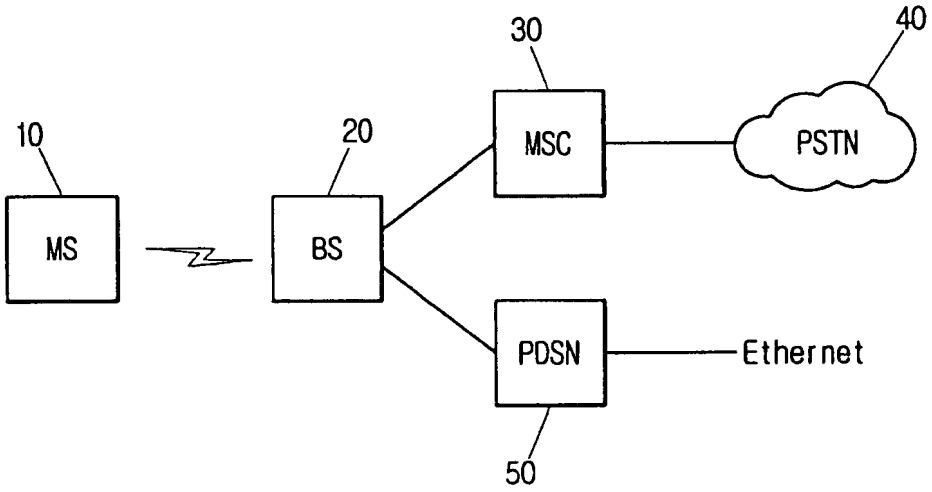
OIPE IAP97
JUL 31 2007
PATENT & TRADEMARK OFFICE

Fig. 1

RELATED ART



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Replacement Sheet

Fig. 2A

RELATED ART

	MuxPDU Header						Permitted on			
Transmit Rate (bits/sec)	Mixed Mode (MM)	Traffic Type (TT)	Traffic Mode (TM)	Primary Traffic (bits/ block)	Signaling Traffic (bits/ block)	Secondary Traffic (bits/block)	F C H	D C C H	S C C H	S C H
9600	'0'	-	-	171	0	0	Y	Y	Y	Y
	'1'	'0'	'00'	80	88	0	Y	Y	N	N
	'1'	'0'	'01'	40	128	0	Y	Y	N	N
	'1'	'0'	'10'	16	152	0	Y	Y	N	N
	'1'	'0'	'11'	0	168	0	Y	Y	N	N
	'1'	'1'	'00'	80	0	88	Y	Y	N	N
	'1'	'1'	'01'	40	0	128	Y	Y	N	N
	'1'	'1'	'10'	16	0	152	Y	Y	N	N
	'1'	'1'	'11'	0	0	168	Y	Y	Y	Y
4800	-	-	-	80	0	0	Y	N	N	N
2400/ 2700	-	-	-	40	0	0	Y	N	N	N
1200/ 1500	-	-	-	16	0	0	Y	N	N	N

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Fig. 2B

RELATED ART

	MuxPDU Header					Permitted on			
Transmit Rate (bits/sec)	Mixed Mode (MM)	Frame Mode (FM)	Primary Traffic (bits/ block)	Signaling Traffic (bits/ block)	Secondary Traffic (bits/block)	FCH	DCCH	SCCH	SCH
14400	'0'	-	266	0	0	Y	Y	Y	Y
	'1'	'0000'	124	138	0	Y	Y	N	N
	'1'	'0001'	54	208	0	Y	Y	N	N
	'1'	'0010'	20	242	0	Y	Y	N	N
	'1'	'0011'	0	262	0	Y	Y	N	N
	'1'	'0100'	124	0	138	Y	Y	N	N
	'1'	'0101'	54	0	208	Y	Y	N	N
	'1'	'0110'	20	0	242	Y	Y	N	N
	'1'	'0111'	0	0	262	Y	Y	Y	Y
	'1'	'1000'	20	222	20	Y	Y	N	N
7200	'0'	-	124	0	0	Y	N	N	N
	'1'	'000'	54	67	0	Y	N	N	N
	'1'	'001'	20	101	0	Y	N	N	N
	'1'	'010'	0	121	0	Y	N	N	N
	'1'	'011'	54	0	67	Y	N	N	N
	'1'	'100'	20	0	101	Y	N	N	N
	'1'	'101'	0	0	121	Y	N	N	N
	'1'	'110'	20	81	20	Y	N	N	N
3600	'0'	-	54	0	0	Y	N	N	N
	'1'	'00'	20	32	0	Y	N	N	N
	'1'	'01'	0	52	0	Y	N	N	N
	'1'	'10'	20	0	32	Y	N	N	N
	'1'	'11'	0	0	52	Y	N	N	N
1800	'0'	-	20	0	0	Y	N	N	N
	'1'	-	0	0	20	Y	N	N	N

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Replacement Sheet

Fig. 2C

RELATED ART

	MuxPDU Header		Traffic bits/ block
	sr_id	Reserved	
Rate Set 1	'001'-'110'	'000'	170/346
	'111'	'000'	0
Rate Set 2	'001'-'110'	'000'	266/538
	'111'	'000'	0